# REGISTRATION FORM for Two Day Workshop on wer Quality –Harmonic Analysi

# "Power Quality –Harmonic Analysis for Power Systems"

(Under TEQIP Phase – II) 19<sup>th</sup> & 20<sup>th</sup> September 2016

1. Name:		
Prof./Dr./Mr./N	/Is	
2. Age (years)_		
3. Qualification	ns	
4. Designation_		
5. Department		
6. Institution		
7. Experience (	in years)	
Teaching		
Industry		
8. Address:		
Phone:		
Email:		
9. D.D. Particu	larsAmount Rs.:	Date:
D.D. No:	Bank:	
Date	Signature of the app	olicant
Signature of the	e Sponsoring Authori	ty
( Brochure can www.uceou.ed	be downloaded from lu )	ı our website

# Registration Fee

**Engineers from Industry** 

: Rs. 2000/-

**Teachers from Academic Institutions** 

: Rs. 1500/-

Research Scholars / Students

: Rs. 1000/-

The Payment of Registration fee is to be made by a Demand Draft in favour of "Head, Department of Electrical Engineering, UCE (A), OU".

Filled-in application forms may also be sent by e-mail with a scanned copy of the DD to gvnlakshmi@rediffmail.com. Original DD should be submitted at the time of Registration.

# **Important Dates**

Last Date for

Receipt of Applications: 13th Sept.,, 2016

Intimation of

Selected Candidates 15<sup>th</sup> Sept.,2016

## **Contact Person**

Mrs. G.V.Naga Lakshmi Assistant Professor

Dept. of Electrical Engineering UCE(A), Osmania University, Hyderabad - 500007 Phone: 9492047114

E-mail: gvnlakshmi@rediffmail.com

# Accommodation & Transport

Accommodation will be provided on request for the outside Male and Female participants in the hostel on payment basis (with nominal charges).

Two Day Workshop on

# "Power Quality –Harmonic Analysis for Power Systems"

(Under TEQIP Phase – II)

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## DEPARTMENT OF ELECTRICAL ENGINEERING

University College of Engineering (A)
Osmania University, Hyderabad – 500 007

#### About the course:

Awareness of power quality is highly increased in sensitive industry where the standardization and performance is an important aspect. The proliferation of power electronic devices and nonlinear loads in electric power networks has triggered a growing concern for power quality issues from both utilities and power users. Tripping of equipment due to disturbances in the supply voltage is often described by the customers as "bad power quality". Utilities on the other side, often view disturbances due to end-user equipment as the main power quality problem.

Power quality problems have increasingly become a substantial concern over the last decade, but surprisingly few analytical techniques have been developed to overcome these disturbances in system-equipment interactions. This two day workshop aims at exposing the faculty members and participants to various power quality issues. A hands on session on ETAP software is being organized during this workshop.

ETAP stands for Electrical Transient & Analysis Program. ETAP is a software program for electrical engineers. ETAP software provides engineers, operators, and managers a platform for continuous functionality from modeling to operation. ETAP's model-driven architecture enables 'Faster than Real-Time' operations - where data and analytics meet to provide predictive behavior, preemptive action and situational intelligence to the owner operator.

Resource Persons Resource persons are drawn from premier institutions like IITs, NITs, Universities, Industries and many other reputed institutions.

*Eligibility:* The workshop is open to Teachers and Research scholars from the institutions recognized and approved by AICTE, Engineers from industry or any other organization.

# About the Department:

The University College of Engineering was established in the year 1929 and celebrated its platinum Jubilee in 2004. The college became autonomous in 1994. The Department of Electrical Engineering was started in the year 1949 and celebrated its Golden Jubilee during the academic year 1999-2000. The post-graduate course in Electrical Machines was started in 1966. With a view to provide support to industry, two more diversifying post graduate programs were introduced in 1971 for both parttime and full-time students. The Ph.D program was introduced in 1972. The Department is well equipped with Circuits Lab, Computer Lab, Control Lab, Integrated Circuits Lab, Microprocessor Lab, Power Electronics Lab and Power Systems Lab. Recently, the Department has established Drives Lab with AICTE funding. The Department has procured latest software, machines and other sophisticated equipment under TEQIP.



#### Chief Patron

Prof. S. Ramachandram, Vice Chancellor, Osmania University.

#### Patron

Prof. Sameen Fathima Principal, UCE (A), Osmania University, Hyderabad.

#### Dean

Prof. Ravande Kishore Faculty of Engineering, Osmania University.

### Chairperson

Dr. B.Mangu Head, Dept. of Electrical Engg., UCE (A) Osmania University, Hyderabad.

#### **Coordinators**

Dr. M.Manjula Mrs. G.V. Naga Lakshmi

# **Advisory Committee**

Prof. P.V.N. Prasad Prof. G. Yesuratnam

#### Technical Committee

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Dr. G. Mallesham
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