A Short Course on

REAL TIME SIGNAL PROCESSING (RTSP-12)

29-31, March 2012

(with Digital Signal Processors, Blackfin and Sharc)

(Course Code : NERTU/SC/50)

DSPs IN MULTIMEDIA and COMMUNICATION ENGINEERING

The demand for Multimedia systems including PCs, set top boxes, portable digital audio (MP3, DVD) players, digital TVs, and digital radios is increasing day by day. This rapid development is possible due to the development in the Digital Signal Processors (DSPs) and Communication Technology. Every year some new algorithms/standards are emerging and there will be some modifications/enhancements to the existing standards for multimedia and other applications. There is a relative dearth of coders. According to Chris Lanfear, director of the embedded software and tools division of research firm Venture Development Corp, (Natick, Mass.), “The number of lines of code is increasing at a CAGR [compound annual growth rate] of 46 percent, but the number of developers is only rising at the rate of 7.5 percent”. Therefore, there is a need for trained people on Digital Signal Processors to develop the upcoming/modified algorithms on DSPs.

ABOUT MMADSPs LABORATORY

By recognizing the potential growth and the need of DSP processors in Multimedia applications and research, Analog Devices came forward to sponsor the Multimedia and ADSPs (MMADSPs) Laboratory at Department of ECE, University College of Engineering, Osmania University, to create professional quality Manpower and promote training, research activities in the broad area of Multimedia and Digital Signal Processors. As part of this project, ADI is sponsoring two full-time research fellowships, Blackfin and Sharc evaluation boards and the required VDSP software. Under this project, earlier three courses were conducted and this is the fourth course in developing quality manpower in the area of multimedia and digital signal processors.

ABOUT THE COURSE

The main objective of the course is to motivate the participants to learn the basics of Digital Signal Processors and to give the idea of writing the programmes in C and Assembly languages for Digital Signal Processors. There will be theory and practical classes on Architecture and Instruction set of Digital Signal Processors in particular to Blackfin and Sharc Processors, VDSP tools, programming examples like FIR filtering, interfacing audio codec with DSP. Scientists/Engineers/Academicians teaching/PG/ Final year UG students working on DSP processors are the expected participants.

Resource Persons:

1. Dr.P.Laxminarayana, NERTU, OU
2. Dr.K.Subba Rao, Stanley College
3. Dr.P.Chandrasekhar, ECE, OU
4. Mr.A.V.Ramana, IKANOS
5. Mr.P.Mythilisharan, ECE, OU

and experts from other institutions and industries.

VENUE: Department of ECE, O.U.

TIME: 0930Hrs – 1730Hrs

REGISTRATION FEE:

Rs. 3000/- for engineers/scientists from industries/research organizations
Rs. 2000/- for teachers/academicians
Rs. 1000/- Full-Time Students

The fee includes lecture notes, working lunch, snacks and tea. The candidates have to make their own arrangements for accommodation.

Seats are limited and filled based on First come first served. Interested candidates can download the registration form from www.osmania.ac.in or from www.uceou.edu and send the filled registration form along with DD/Cheque drawn in favor of “The Chief-Coordinator, Multimedia and ADSPs Laboratory, Dept. of ECE, OU”.

Last date for receiving the applications is 17th March 2012.

Email Ids/Phone Numbers for correspondence:

Dr.P. Laxminarayana plaxminarayana@yahoo.com Ph: 949 080 5486
Dr.K. Subba Rao kakarlasubbarao@yahoo.com Ph: 944 011 5130
N. Rajaiah, Ph :040-27098213