# TIME TABLE

<table>
<thead>
<tr>
<th>Days\Time</th>
<th>9:00 to 10:00</th>
<th>10:00 to 11:00</th>
<th>11:00 to 12:00</th>
<th>12:00 to 13:00</th>
<th>14:00 to 15:00</th>
<th>15:00 to 16:00</th>
<th>16:00 to 17:00</th>
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<tbody>
<tr>
<td>Monday</td>
<td>****</td>
<td>MIII (VK)</td>
<td>DM (VBN)</td>
<td>DS (VS)</td>
<td>CO (KSR)</td>
<td>Basic Electronic Engineering Lab Batch – I (RNR/PS)</td>
<td>Electrical Engineering Lab Batch – II (GVNL/GJR)</td>
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<tr>
<td>Tuesday</td>
<td>LST (MVD)</td>
<td>DS (VS)</td>
<td>DM (VBN)</td>
<td>CO (KSR)</td>
<td>DS Lab Batch – I SII Lab (VS / Swetha)</td>
<td>Batch – II SIV Lab (KSR / Rani)</td>
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<tr>
<td>Wednesday</td>
<td>****</td>
<td>MIII (VK)</td>
<td>LST (MVD)</td>
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<td>DS (VS)</td>
<td>Basic Electronic Engineering Lab Batch – III (RNR/PS)</td>
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<tr>
<td>Thursday</td>
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</tbody>
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## KEY TO SUBJECTS

### THEORY
1. DS – Data Structures
2. DM – Discrete Mathematics
3. LST – Logic & Switching Theory
4. CO – Computer Organization
5. MIII – Mathematics III

### LIST OF FACULTY

1. VS – Mrs.V.Sukanya
2. VBN – Dr.V.B.Narsimha
3. MVD - Mr.M.Venkat Dass
4. KSR – Mr.K.Srinivasa Reddy
5. VK – Mr.V.Kiran

### ALMANAC

- Commencement of Instruction: 13th July - 2015
- Annual Academic Awards Function: 22nd August - 2015
- First Class Tests: 3rd to 5th September - 2015
- Engineers Day: 15th September - 2015
- Dussara Vacation: 19th to 24th October - 2015
- Second Class Tests: 2nd to 4th November - 2015
- Last Date of Instruction: 7th November - 2015
- Preparation Holidays and Practical Examinations: 9th to 21st November - 2015
- Theory Examinations (Main and Supple.): 23rd November to 14th December - 2015

### PRACTICALS

1. DS Lab - Data Structures lab
2. BE Lab - Basic Electronics Lab
3. EE Lab – Electrical Engineering Lab

*Suggested Spoken Software as E-Resource for learning: http://spoken-tutorial.org*

Data Structures lab: C++
Basic Electronics lab: Oscad@FreeEDA