Department of Computer Science & Engineering
University College of Engineering (A), Osmania University, Hyd-7
Academic Year 2015-2016
M.Tech (ESC) - I Semester (REGULAR)
Class Venue – B 206

Time Table

DAYS | 9.00 TO 10-00 | 10.00 TO 11-00 | 11-00 TO 12.00 | 12.00 TO 13.00 | 14.00 to 15.00 | 15.00 TO 16.00 | 16.00 TO 17.00
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MON | ************ | ESD (VR) | DSD (KSH) | GTA (AG) | MES (PCS) | ************ | ************
TUE | ************ | RTOS (SRC) | ESD (VR) | GTA (AG) | MES (PCS) | ************ | ************
WED | ************ | RTOS (SRC) | SL (IGR) | ESD (VR) | Seminar – I | Software Lab – I | (KSH)
THU | SL (IGR) | RTOS (SRC) | DSD (KSH) | GTA (AG) | Make up Exams | 7 to 26th March - 2016
FRI | ************ | ************ | DSD (KSH) | SL (IGR) | MES (PCS) | ************ | ************

KEY TO SUBJECT

THEORY
1. ESD – Embedded System Design
2. DSD – Digital System Design
3. MES – Microcontroller for Embedded Systems
4. RTOS- Real Time Operating Systems
5. SL – Scripting Languages for Design Automation
6. GTA – Graph theory and its Applications

Elective I
5. IGR – Mr. I.Govardhana Rao

Elective II
6. AG – Mrs. A. Gayathri

LIST OF FACULTY
1. VR – Prof. Mr. Veerabhadra Rao
2. KSH – Dr. K. Shyamala
3. PCS – Dr. P. Chandra Shekar
4. SRC – Prof. S. Ramachandram

Practicals
SW Lab - I - KSH- Dr. K. Shyamala
Seminar – I - VR – Prof. Mr. Veerabhadra Rao / AG – Mrs. A. Gayathri

ALMANAC
Commencement of Classes : 14th September-2015
Class Test I : 5th to 7th November - 2015
Class Test II : 4th to 6th January - 2016
Last day of Instructions : 9th January - 2016
Theory Exams : 18th January to 6th February -2016
Make up Exams : 7 to 26th March - 2016

Dr. K. Shyamala
M.Tech. Faculty Advisor,
Dr. K. Shyamala
M.Tech. Faculty Advisor,
Mr. S. Ram Babu
Head,
Prof. S. Ramachandram
Principal