Dr. P. Chandra Sekhar

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Dr.B.Rajendra Naik Publication Details

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1. Sudheer Kumar Yezerla, B.**Rajendra Naik**, "Design and Estimation of delay, power and area for Parallel prefix adders" presented in international Conference on Recent Advances in Engineering and Computational Sciences (RAECS 2014) March 6-8 2014 UIET Panjab University Chandigarh, India.
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