### UNIVERSITY COLLEGE OF ENGINEERING
OSMANIA UNIVERSITY, HYDERABAD
M.E. (E.C.E) II - Semester (Regular & PTPG) and IV-Semester (PTPG) Make-up Examination
January/February 2013
EXAMINATION TIME TABLE
Time : 2.00 PM to 5.00 PM

<table>
<thead>
<tr>
<th>Date and Day</th>
<th>Digital Systems</th>
<th>Systems &amp; Signal Processing</th>
<th>Microwave &amp; Radar Engineering</th>
<th>Embedded Systems and VLSI</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Regular II-Semester</td>
<td>PTPG II-Semester</td>
<td>PTPG IV-Semester</td>
<td>Regular II-Semester</td>
</tr>
<tr>
<td>28-01-2013 MONDAY</td>
<td>Low Power VLSI Design</td>
<td>= = = = = =</td>
<td>Digital Control</td>
<td>Digital Control</td>
</tr>
<tr>
<td>01-02-2013 FRIDAY</td>
<td>Digital Design &amp; PLD’s</td>
<td>Digital Design &amp; PLD’s</td>
<td>= = = = = =</td>
<td>System on Chip Architecture</td>
</tr>
<tr>
<td>06-02-2013 WEDNESDAY</td>
<td>= = = = = =</td>
<td>VLSI Technology</td>
<td>Adaptive Signal Processing</td>
<td>Adaptive Signal Processing</td>
</tr>
</tbody>
</table>

**Note:** This Time-Table is also applicable for Pre-Ph.D. students registered for APRIL 2012 Examination.

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