

UNIVERSITY COLLEGE OF ENGINEERING (A)
OSMANIA UNIVERSITY, HYDERABAD – 500007

B.E. IV Year - I-SEMESTER (MAIN) PRACTICAL EXAMS. OCTOBER/NOVEMBER-2017

Name of the Lab.

Date	Time	Hall Tickets Nos.	From ----- To	Strength
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BIOMEDICAL ENGINEERING

BIO-SIGNAL AND IMAGE PROCESSING LAB

02.11.2017 Thursday	08.00 – 11.00 hrs	1005-14-731001 to 1004, 1006 to 1011	(10)
	11.00 – 14.00 hrs.	1005-14-731012 to 1021	(10)
	14.00 – 17.00 hrs	1005-14-731022 to 1025, 1028, 1029,1081 to 1083 & backlogs	(09)

MEDICAL EMBEDDED SYSTEM LAB

03.11.2017 Friday	08.00 – 11.00 hrs.	1005-14-731001 to 1004, 1006 to 1011	(10)
	11.00 – 14.00 hrs.	1005-14-731012 to 1021	(10)
	14.00 – 17.00 hrs	1005-14-731022 to 1025, 1028, 1029,1081 to 1083 & backlogs	(09)

Note :

1. Any omission or clash in the practicals timetable may please be brought to the notice of the Chief Superintendent immediately by the concerned candidate.

2. These practicals time table is also applicable for the **backlog/supplementary** students whose numbers are not listed above

Chief Superintendent
B.E. Main Exams 2017

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OSMANIA UNIVERSITY, HYDERABAD – 500007**

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Name of the Lab.

Date	Time	Hall Tickets Nos.	From ----- To	Strength
<u>CIVIL ENGINEERING</u>				
COMPUTER APPLICATIONS LAB				
30.10.2017 Monday	08.00 – 11.00 hrs.	1005-14-732001 to 2010, 2012,2013		(12)
	11.00 – 14.00 hrs.	1005-14-732014 to 2024		(11)
	14.00 – 17.00 hrs	1005-14-732025,2026, 2028, 2030 to 2037		(11)
31.10.2017 Tuesday	08.00 – 11.00 hrs.	1005-14-732038 to 2040, 2042 to 2050		(12)
	11.00 – 14.00 hrs.	1005-14-732051 to 2059, 2061,2062		(11)
	14.00 – 17.00 hrs	1005-14-732069,2072,2073, 2082 to 2086, 1005-732067, 2073, 1005-11-732010 & backlogs		(10)
CONCRETE TECHNOLOGY LAB				
30.10.2017 Monday	08.00 – 11.00 hrs.	1005-14-732038 to 2040, 2042 to 2050		(12)
	11.00 – 14.00 hrs.	1005-14-732051 to 2059, 2061,2062		(11)
	14.00 – 17.00 hrs	1005-14-732069,2072,2073, 2082 to 2086, 1005-732067, 2073, 1005-11-732010 & backlogs		(10)
31.10.2017 Tuesday	08.00 – 11.00 hrs.	1005-14-732001 to 2010, 2012,2013		(12)
	11.00 – 14.00 hrs.	1005-14-732014 to 2024		(11)
	14.00 – 17.00 hrs	1005-14-732025,2026, 2028, 2030 to 2037		(11)
Note : 1. Any omission or clash in the practicals timetable may please be brought to the notice of the Chief Superintendent immediately by the concerned candidate.				
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Chief Superintendent B.E. Main Exams 2017				

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Date	Time	Hall Tickets Nos.	From ----- To	Strength
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COMPUTER SCIENCE ENGINEERING

OBJECT ORIENTED ANALYSIS & DESIGN LAB & COMPILER CONSTRUCTION LAB

30.10.2017 Monday	08.00 – 11.00 hrs.	1005-14-733001 to 3025	(25)
	11.00 – 14.00 hrs.	1005-14-733026 to 3047, 3049 to 3051	(25)
	14.00 – 17.00 hrs	1005-14-733052 to 3060, 3062 to 3064, 3067, 3068, 3070 to 3072, 3081 to 3087 & backlogs	(24)

DISTRIBUTED SYSTEMS LAB

31.10.2017 Tuesday	08.00 – 11.00 hrs.	1005-14-733001 to 3025	(25)
	11.00 – 14.00 hrs.	1005-14-733026 to 3047, 3049 to 3051	(25)
	14.00 – 17.00 hrs	1005-14-733052 to 3060, 3062 to 3064, 3067, 3068, 3070 to 3072, 3081 to 3087 & backlogs	(24)

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Chief Superintendent
B.E. Main Exams 2017

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B.E. IV Year - I-SEMESTER (MAIN) PRACTICAL EXAMS. OCTOBER/NOVEMBER-2017

Name of the Lab.

Date	Time	Hall Tickets Nos.	From ----- To	Strength
<u>ELECTRONICS & COMMUNICATION ENGINEERING</u>				
<u>MICROWAVE LAB</u>				
30.10.2017 Monday	08.00 – 11.00 hrs.	1005-14-735001 to 5007		(07)
	11.00 – 14.00 hrs.	1005-14-735008 to 5013		(06)
	14.00 – 17.00 hrs	1005-14-735014 to 5019		(06)
31.10.2017 Tuesday	08.00 – 11.00 hrs.	1005-14-735020 to 5026		(07)
	11.00 – 14.00 hrs.	1005-14-735027 to 5032		(06)
	14.00 – 17.00 hrs	1005-14-735033 to 5036, 5038,5039		(06)
01.11.2017 Wednesday	08.00 – 11.00 hrs.	1005-14-735040 to 5042, 5044 to 5046		(06)
	11.00 – 14.00 hrs.	1005-14-735047 to 5051, 5053		(06)
	14.00 – 17.00 hrs	1005-14-735081 to 5085 & backlogs		(05)
<u>ELECTRONIC DESIGN AND AUTOMATION LAB</u>				
30.10.2017 Monday	08.00 – 11.00 hrs.	1005-14-735020 to 5026		(07)
	11.00 – 14.00 hrs.	1005-14-735027 to 5032		(06)
	14.00 – 17.00 hrs	1005-14-735033 to 5036, 5038,5039		(06)
31.10.2017 Tuesday	08.00 – 11.00 hrs.	1005-14-735040 to 5042, 5044 to 5046		(06)
	11.00 – 14.00 hrs.	1005-14-735047 to 5051, 5053		(06)
	14.00 – 17.00 hrs	1005-14-735081 to 5085 & backlogs		(05)
01.11.2017 Wednesday	08.00 – 11.00 hrs.	1005-14-735001 to 5007		(07)
	11.00 – 14.00 hrs.	1005-14-735008 to 5013		(06)
	14.00 – 17.00 hrs	1005-14-735014 to 5019		(06)
Note : 1. Any omission or clash in the practicals timetable may please be brought to the notice of the Chief Superintendent immediately by the concerned candidate.				
2. These practicals time table is also applicable for the backlog/supplementary students whose numbers are not listed above.				
Chief Superintendent B.E. Main Exams 2017				

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Name of the Lab.

Date	Time	Hall Tickets Nos.	From ----- To	Strength
<u>ELECTRICAL & ELECTRONICS ENGINEERING</u>				
SIMULATION LAB				
30.10.2017 Monday	08.00 – 11.00 hrs.	1005-14-734001 to 4011		(11)
	11.00 – 14.00 hrs.	1005-14-734012 to 4022		(11)
	14.00 – 17.00 hrs	1005-14-734023 to 4033		(11)
31.10.2017 Tuesday	08.00 – 11.00 hrs.	1005-14-734034 to 4038, 4040 to 4045		(11)
	11.00 – 14.00 hrs.	1005-14-734046 to 4056		(11)
	14.00 – 17.00 hrs	1005-14-734057 to 4059, 4061, 4062, 4081, 4082, 4084 to 4086, 1005-13-734061 & backlogs		(11)
MICROPROCESSOR LAB & MICROCONTROLLER				
30.10.2017 Monday	08.00 – 11.00 hrs.	1005-14-734034 to 4038, 4040 to 4045		(11)
	11.00 – 14.00 hrs.	1005-14-734046 to 4056		(11)
	14.00 – 17.00 hrs	1005-14-734057 to 4059, 4061, 4062, 4081, 4082, 4084 to 4086, 1005-13-734061 & backlogs		(11)
31.10.2017 Tuesday	08.00 – 11.00 hrs.	1005-14-734001 to 4011		(11)
	11.00 – 14.00 hrs.	1005-14-734012 to 4022		(11)
	14.00 – 17.00 hrs	1005-14-734023 to 4033		(11)
Note : 1. Any omission or clash in the practicals timetable may please be brought to the notice of the Chief Superintendent immediately by the concerned candidate.				
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Chief Superintendent B.E. Main Exams 2017				

Revised

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Name of the Lab.

Date	Time	Hall Tickets Nos.	From ----- To	Strength
<u>MECHANICAL ENGINEERING</u>				
THERMAL ENGINEERING LAB				
30.10.2017 Monday	08.00 – 11.00 hrs.	1005-14-736001, 6003 to 6010		(09)
	11.00 – 14.00 hrs.	1005-14-736011 to 6017, 6019		(08)
	14.00 – 17.00 hrs	1005-14-736020 to 6027		(08)
31.11.2017 Tuesday	08.00 – 11.00 hrs.	1005-14-736028 to 6035		(08)
	11.00 – 14.00 hrs.	1005-14-736036 to 6038, 6040 to 6044		(08)
	14.00 – 17.00 hrs	1005-14-736045, 6046, 6048 to 6052,6054		(08)
01.11.2017 Wednesday	08.00 – 11.00 hrs.	1005-14-736055 to 6059, 6061 to 6064		(09)
	11.00 – 14.00 hrs.	1005-14-736065 to 6066, 6068 to 6070, 6073, 6077		(08)
	14.00 – 17.00 hrs	1005-14-736081, 6083 to 6086, 1005-13736038, 6060, 1005-11-736062 & backlogs		(08)
CAD/CAM LAB				
31.10.2017 Tuesday	08.00 – 11.00 hrs.	1005-14-736055 to 6059, 6061 to 6064		(09)
	11.00 – 14.00 hrs.	1005-14-736065 to 6066, 6068 to 6070, 6073, 6077		(08)
	14.00 – 17.00 hrs	1005-14-736081, 6083 to 6086, 1005-13736038, 6060, 1005-11-736062 & backlogs		(08)
01.11.2017 Wednesday	08.00 – 11.00 hrs.	1005-14-736001, 6003 to 6010		(09)
	11.00 – 14.00 hrs.	1005-14-736011 to 6017, 6019		(08)
	14.00 – 17.00 hrs	1005-14-736020 to 6027		(08)
02.11.2017 Thursday	08.00 – 11.00 hrs.	1005-14-736028 to 6035		(08)
	11.00 – 14.00 hrs.	1005-14-736036 to 6038, 6040 to 6044		(08)
	14.00 – 17.00 hrs	1005-14-736045, 6046, 6048 to 6052,6054		(08)
Note : 1. Any omission or clash in the practicals timetable may please be brought to the notice of the Chief Superintendent immediately by the concerned candidate.				
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Chief Superintendent B.E. Main Exams 2017				