

UNIVERSITY COLLEGE OF ENGINEERING (A)
OSMANIA UNIVERSITY, HYDERABAD – 500007
B.E. IV Year II-SEMESTER (MAIN) PRACTICAL EXAMS. APRIL-2017

Name of the Lab.

Date	Time	Hall Tickets Nos.	From ----- To	Strength
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<u>COMPUTER SCIENCE ENGINEERING</u>				
Embedded Systems Lab				
20.04.2017 Thursday	08.00 – 11.00 hrs.	1005-13-733001 to 3023, 3025 to 2027		(26)
	11.00 – 14.00 hrs.	1005-13-733028 to 3038, 3040 to 3054		(26)
	14.00 – 17.00 hrs	1005-13-7303055 to 3065, 3069 to 3072, 3075, 3081 to 3086, 1005-12-733026, 3061 & backlogs		(26)
Note :				
1. Any omission or clash in the practicals timetable may please be brought to the notice of the Chief Superintendent immediately by the concerned candidate.				
2. These practicals time table is also applicable for the backlog/supplementary students whose numbers are not listed below.				
Chief Superintendent B.E. Main Exams 2017				

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ELECTRICAL & ELECTRONICS ENGINEERING

Power Systems Lab

01.05.2017 Monday	08.00 – 11.00 hrs.	1005-13-734001 to 4008	(08)
	11.00 – 14.00 hrs.	1005-13-734009 to 4016	(08)
	14.00 – 17.00 hrs	1005-13-734017 to 4023	(07)
02.05.2017 Tuesday	08.00 – 11.00 hrs.	1005-13-734024 to 4031	(08)
	11.00 – 14.00 hrs.	1005-13-734032 to 4039	(08)
	14.00 – 17.00 hrs	1005-13-734040 to 4046	(07)
03.05.2017 Wednesday	08.00 – 11.00 hrs.	1005-13-734047 to 4054	(08)
	11.00 – 14.00 hrs.	1005-13-734055 to 4057, 4059,4060,4062, 4063, 4065	(08)
	14.00 – 17.00 hrs	1005-13-734066, 4081 to 4083, 4085, 4086,100512-734053 & backlogs	(07)

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Chief Superintendent
B.E. Main Exams 2017